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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application / Conf. No.	10/619,136 / 6066
		Filing Date	July 14, 2003
		First Named Inventor	David Mark
		Examiner Name	Jimmy Nguyen
		Art Unit	2829
		Patent No.	
Total Number of Pages in This Submission		Attorney Docket Number	X-1269-1P US

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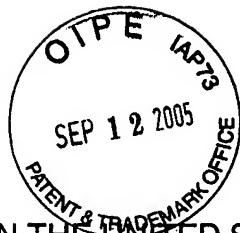
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X-1269-1P US
10/619,136



PATENT
Conf. No. 6066

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: David Mark et al.

Assignee: Xilinx, Inc.

Title: Method for Gross I/O Functional Test at Wafer Sort

Serial No.: 10/619,136

Filing Date: 07/14/2003

Examiner: Jimmy Nguyen

Art Unit: 2829

Docket No.: X-1269-1P US

Conf. No.: 6066

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RESPONSE TO THIRD OFFICE ACTION

In response to the Third Office Action mailed from the Patent Office on June 10, 2005, Applicants submit the following remarks.

Listing of Claims begins on page 2 of this paper.

Remarks begin on page 7 of this paper.